

#9/10

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. RA001C10)

In the Application of:)	
)	
FARMWALD ET AL.)	Group
)	Art Unit: 2781
Serial No: 09/514,872)	
)	Before
Filed: FEBRUARY 28, 2000)	Examiner: G. Auve'
)	
Title: METHOD AND APPARATUS FOR)	
CONTROLLING A SYNCHRONOUS)	
MEMORY DEVICE)	

Assistant Commissioner for Patents
Washington, DC 20231

FAX RECEIVED

DEC 01 2000

PETITIONS OFFICE

AMENDMENT

Dear Sir:

Kindly amend the application as follows:

IN THE CLAIMS:

Kindly Amend the following claims:

²
~~152~~. (Amended) The method of claim ¹~~151~~ wherein the write request includes an operation code[, the first portion of data and the second portion of data are included in a packet].

D¹
³
~~153~~. (Amended) The method of claim ²~~152~~ wherein the memory device receives the operation code synchronously with respect to an edge transition of the external clock signal [write request, the first

D